

S/N 10/643,585



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott

Examiner: Sheng Jen Tsai

Serial No.: 10/643,585

Group Art Unit: 2186

Filed: August 18, 2003

Docket No.: 1376.700US1

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY
MULTIPROCESSOR COMPUTER

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on September 26, 2005. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three-month extension of the period for responding to the Office Action, thereby moving the deadline for response from December 26, 2005 to March 27, 2006 (Monday).